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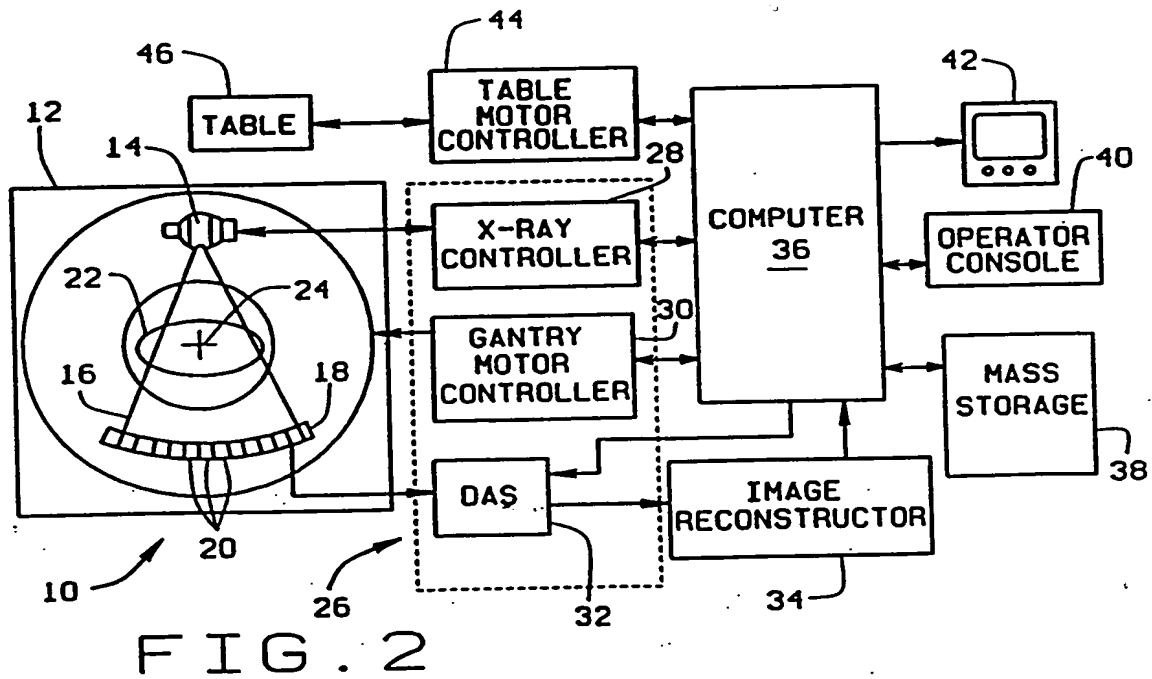
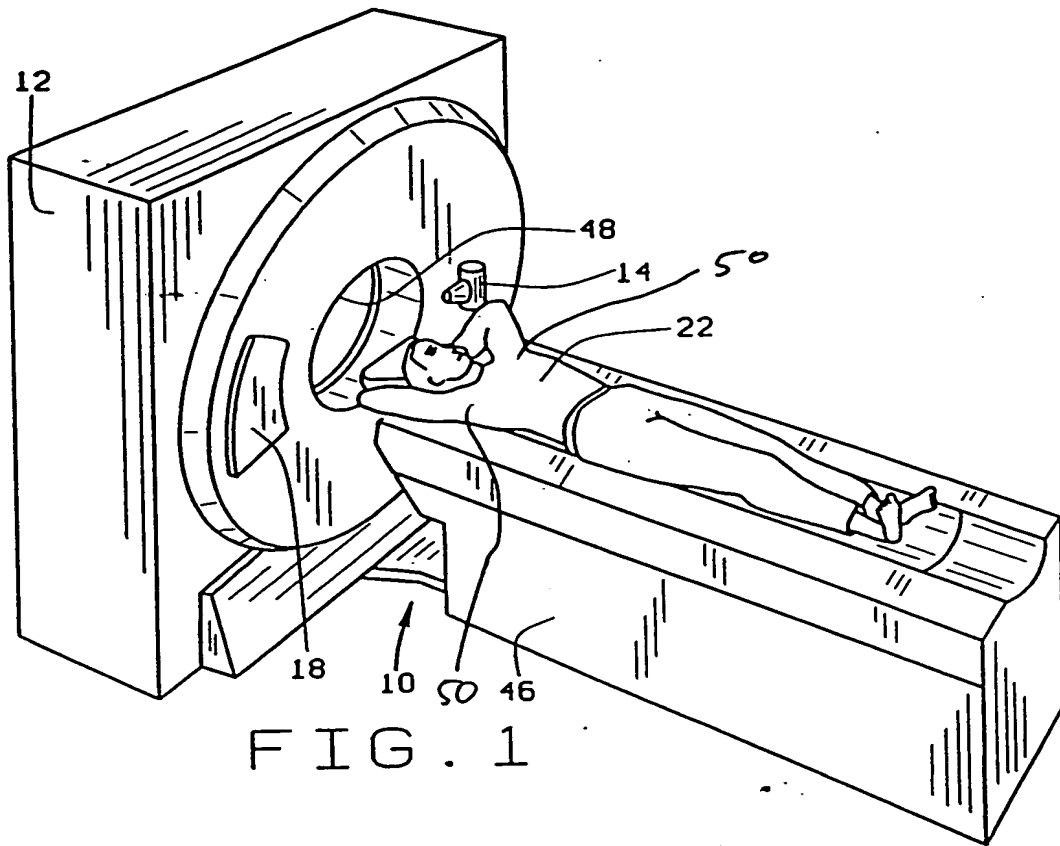


Diagram illustrating the IEEE 754 floating-point format structure:

- Sign bit**: The leftmost bit.
- $e = \text{Exponent}$** : The segment following the sign bit.
- $b_{30} - b_{23}$** : A segment of bits, labeled $i = \text{Region}$.
- $b_{22} - b_{16}$** : A segment of bits, labeled $\Delta x = \text{from sub-region left boundary to } a_i$.
- $b_{15} - b_0$** : The final segment of bits.

Fig 3